

PATENT ABSTRACTS OF JAPAN

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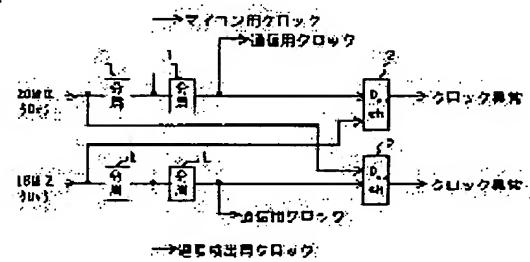
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(54) CONTROL DEVICE FOR ELEVATOR

(57)Abstract:

PURPOSE: To highly precisely detect abnormality, to improve service and to secure safety by mutually monitoring a plural number of clocks used for control of an elevator with each other and detecting abnormality. **CONSTITUTION:** A 20MHz clock for formation of a clock for a microcomputer and a clock for communication and a 16MHz clock for formation of a clock for speed detection and the clock for communication are used for an elevator control device. Each of the clocks is formed by dividing the 20MHz clock and the 16MHz clock by a dividing circuit 1. As all the earlier clocks are thought to be normal if the last divided clock is normal, the last divided clock is detected by a clock abnormality detection circuit 2 by using one of the other clocks. When the divided clock is abnormal, clock abnormality is output. Consequently, highly precise detection is possible as it is detected by a digital circuit. Additionally, as a circuit for clock abnormality detection is not specially provided, it is possible to improve productivity.



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